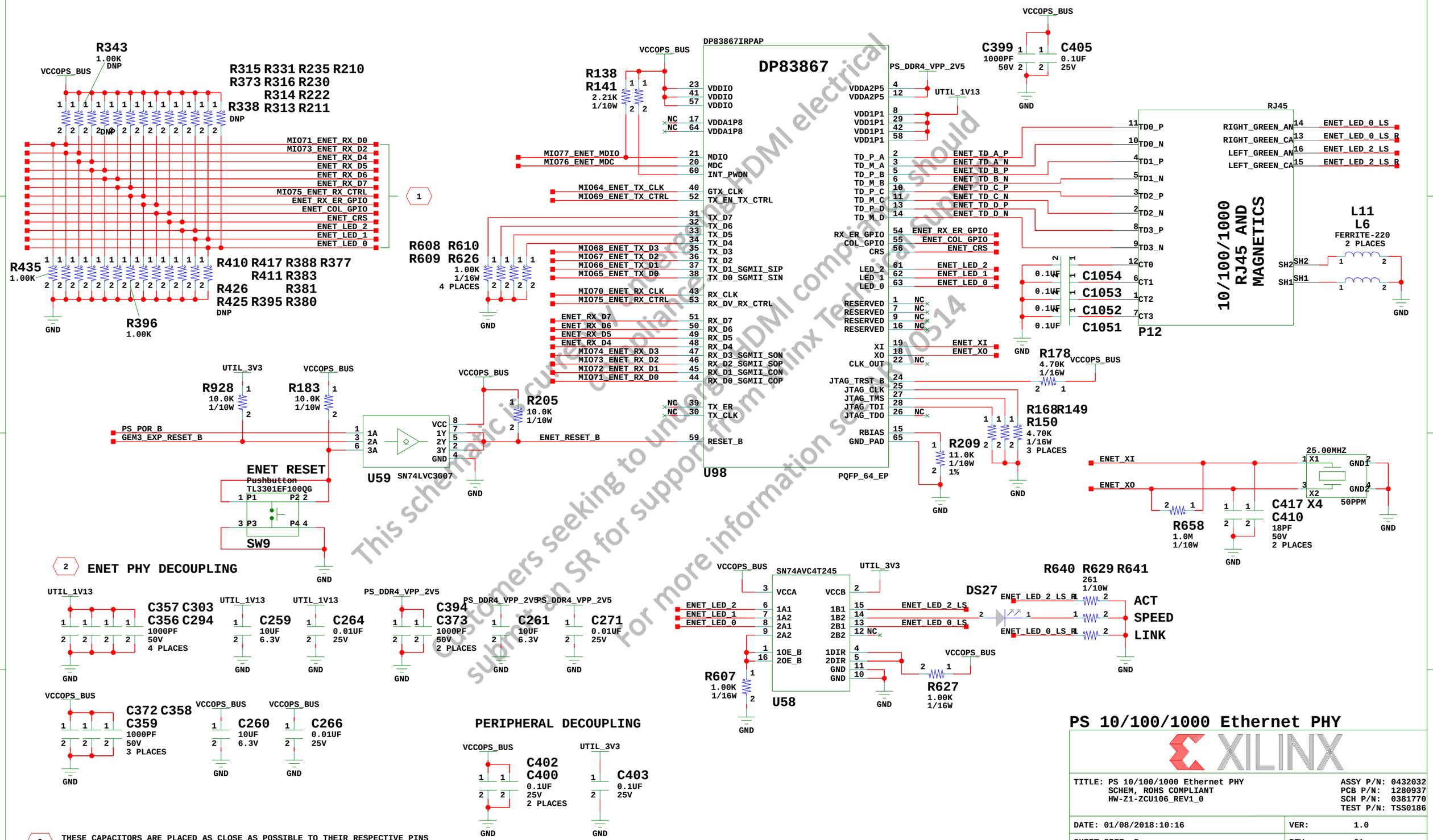

THESE CAPACITORS ARE PLACED AS CLOSE AS POSSIBLE TO THEIR RESPECTIVE PINS ON THE DP83867.

STRAP CONFIGURATION RESISTORS. SEE DP83867 DATASHEET FOR DETAILS.

This schematic is current by concerning HDMI electrical compliance. Customers seeking to understand HDMI compliance should submit an SR for support from Xilinx Technical Support. For more information see AR 70314

PS 10/100/1000 Ethernet PHY

XILINX

TITLE: PS 10/100/1000 Ethernet PHY SCHEM, ROHS COMPLIANT HW-Z1-ZCU106_REV1_0	ASSY P/N: 0432032 PCB P/N: 1280937 SCH P/N: 0381770 TEST P/N: TSS0186
DATE: 01/08/2018:10:16	VER: 1.0
SHEET SIZE: B	REV: 01
SHEET 59 OF 95	DRAWN BY: BF