Figure 2. DP83822 Pinout

There are three pins used by the TLK devices that are not used by the DP83822: PFBIN1, PFBIN2 and PFBOUT. The TLK devices use these pins for the 1.55-V supply by either connecting an external supply or using the internal regulator and connecting decoupling capacitors. The DP83822 has an improved internal regulator design, which no longer requires these pins for functional use. By default, pin #24, #15 and #13 are tri-state.

When using an existing TLK footprint, the DP83822 does not require any board modification because the pins are tri-stated; decoupling capacitors can remain as well as an external supply. However, it is recommended to remove such connections to lower BOM cost and footprint size. Additionally, the DP83822 can operate with pin #24, #15 and #13 floating.